SPECIFICATION AMENDMENTS

Please amend paragraph [0036] as indicated below:

[0036] In a process block 430, processor 305 loads native type ISRs 381 into system memory 310. One such native type ISR is global interrupt handler [[370]] 379. In one embodiment, global interrupt handler 379 is initially stored in BFD 330 and loaded therefrom into upper system memory 360. In one embodiment, global interrupt handler 379 receives all IRQs, both legacy type hardware IRQs and native type hardware IRQs, and invokes the appropriate ISR in response. In one embodiment, global interrupt handler 370 is a native type extensible firmware interface ("EFI") driver compliant with the EFI standard framework (e.g., EFI Specification, version 1.10, December 1, 2002).

Please amend paragraph [0039] as indicated below:

[0039] Turning now to FIGS 3 and 4B, a process 400B describes how legacy type hardware IRQs and native type hardware IRQs are managed when processing system 300 is operating in either the native mode runtime or the legacy mode runtime. ~~Process 400B described how legacy type hardware IRQs and native type hardware IRQs are managed when processing system 300 is operating in either the native mode runtime or the legacy mode runtime.~~